

Figure 1

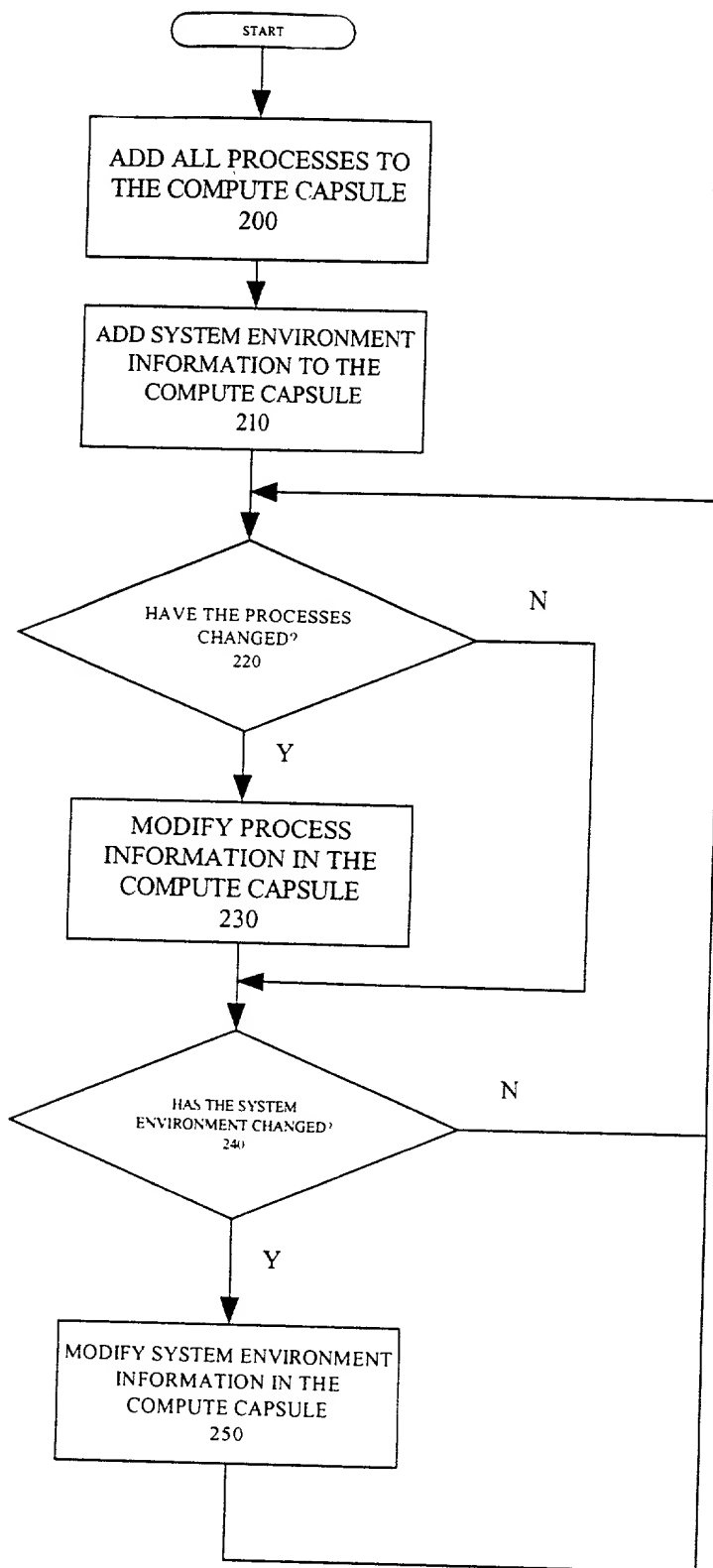


FIGURE 2

FIGURE 3A

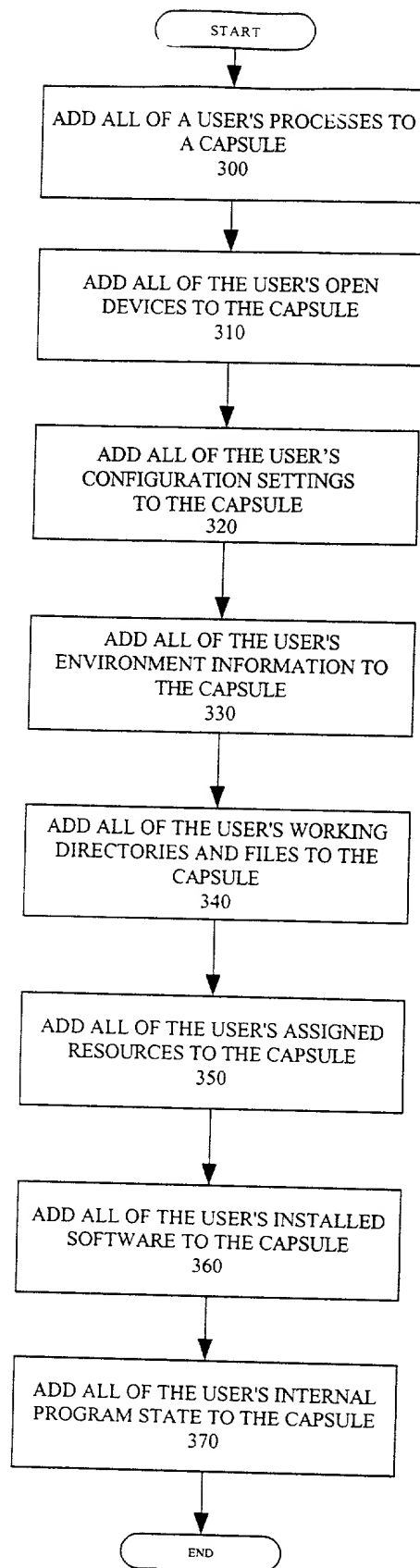
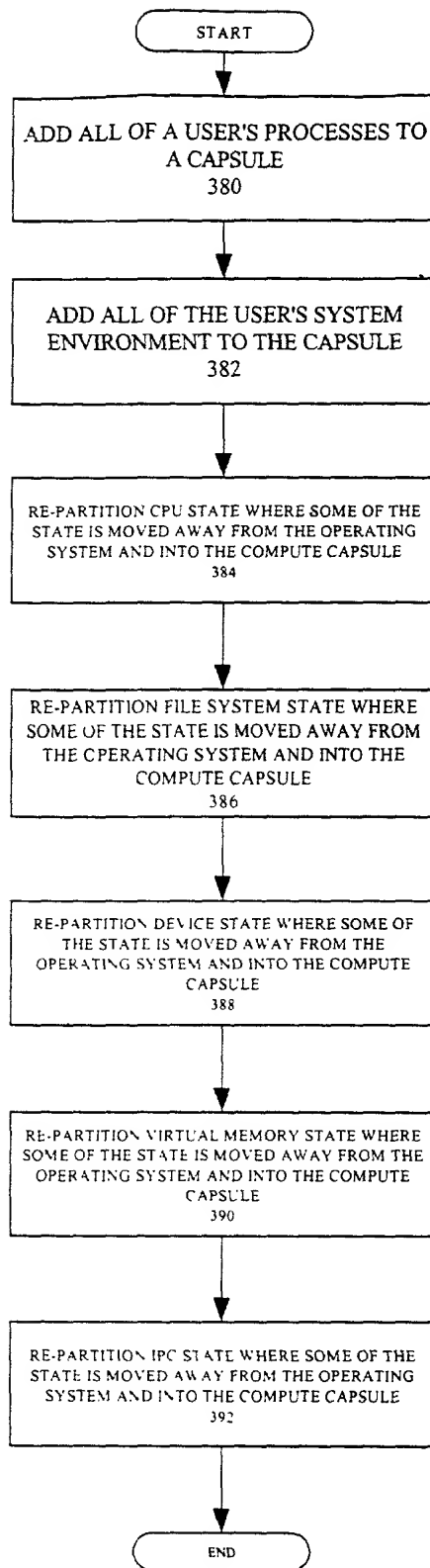


FIGURE 3B



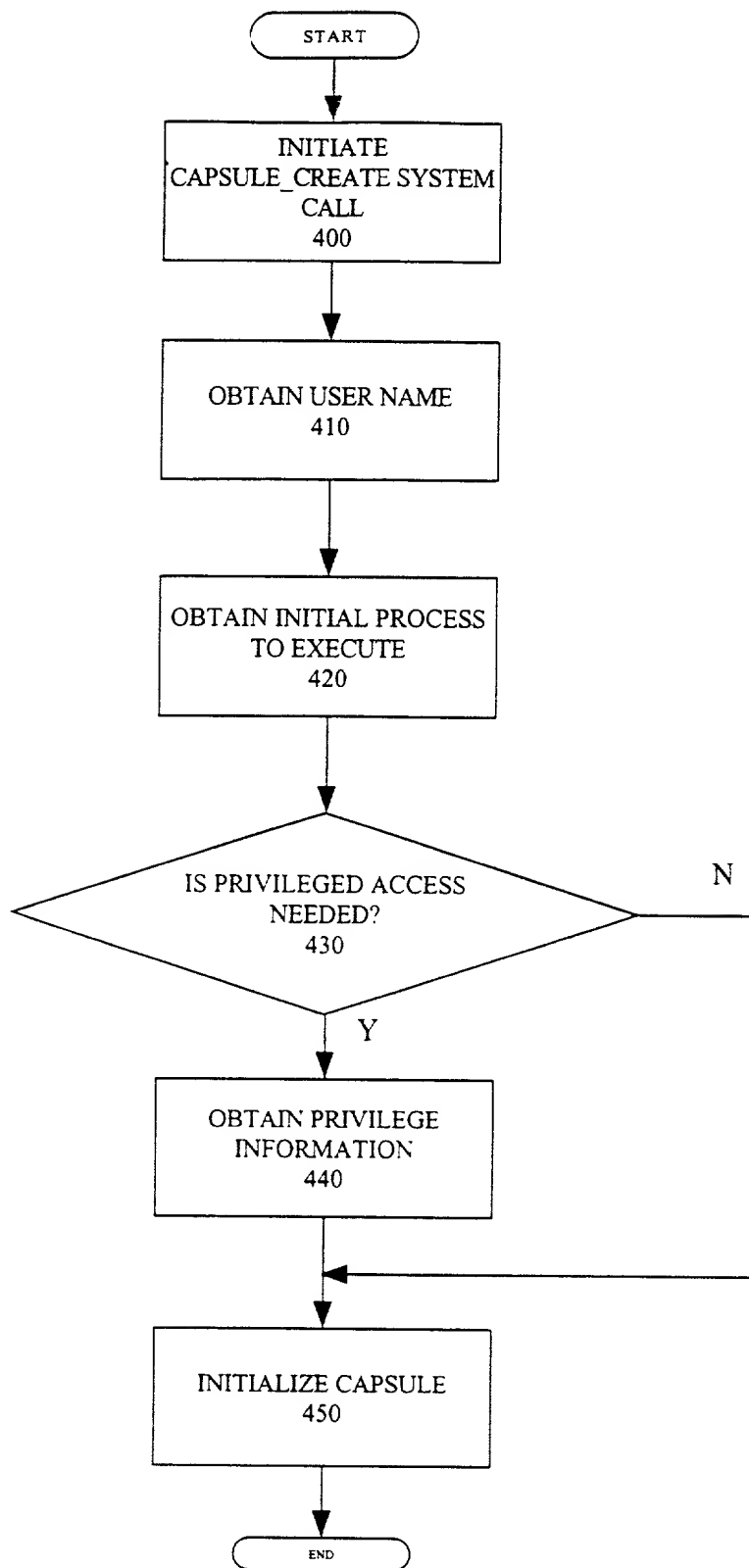


FIGURE 4

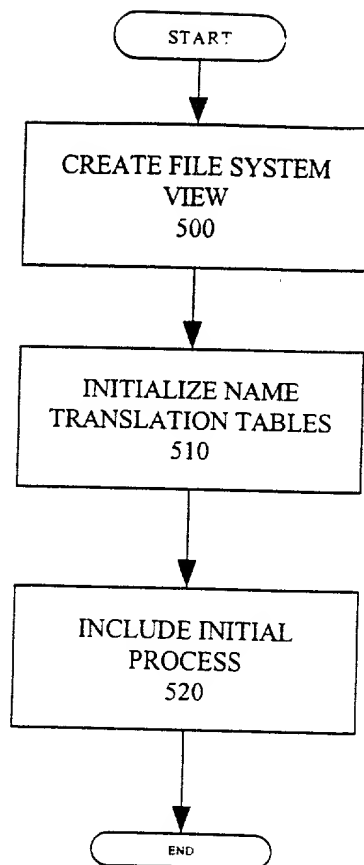


FIGURE 5

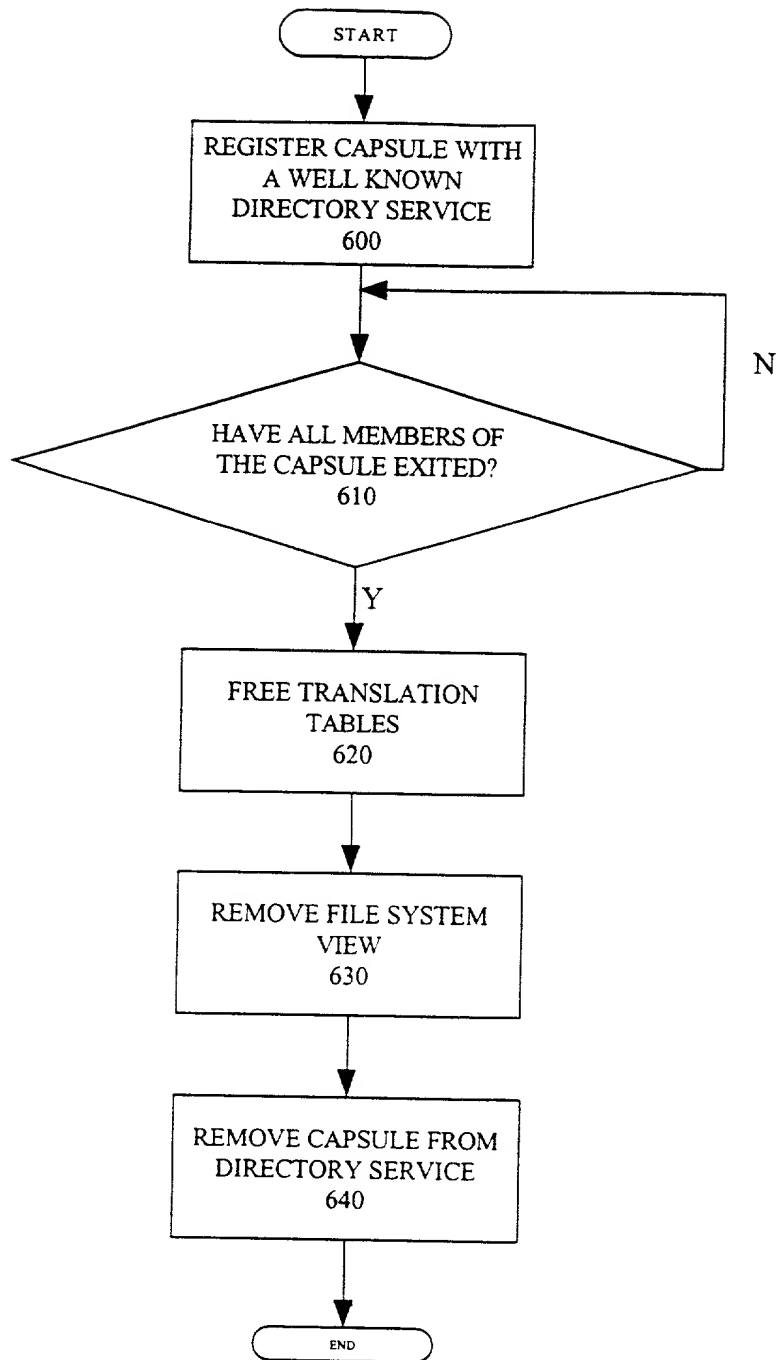


FIGURE 6

FIGURE 7

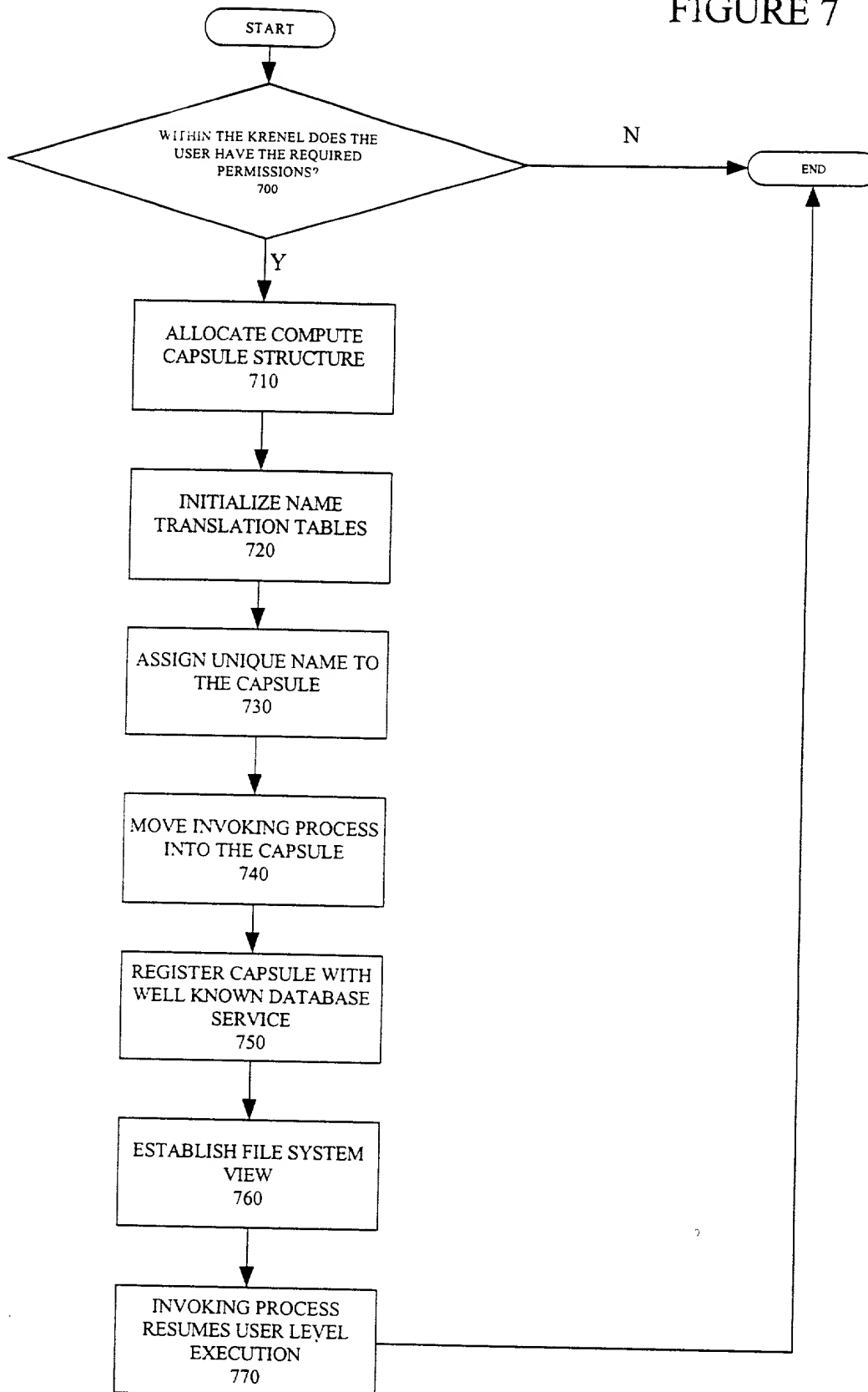


FIGURE 8

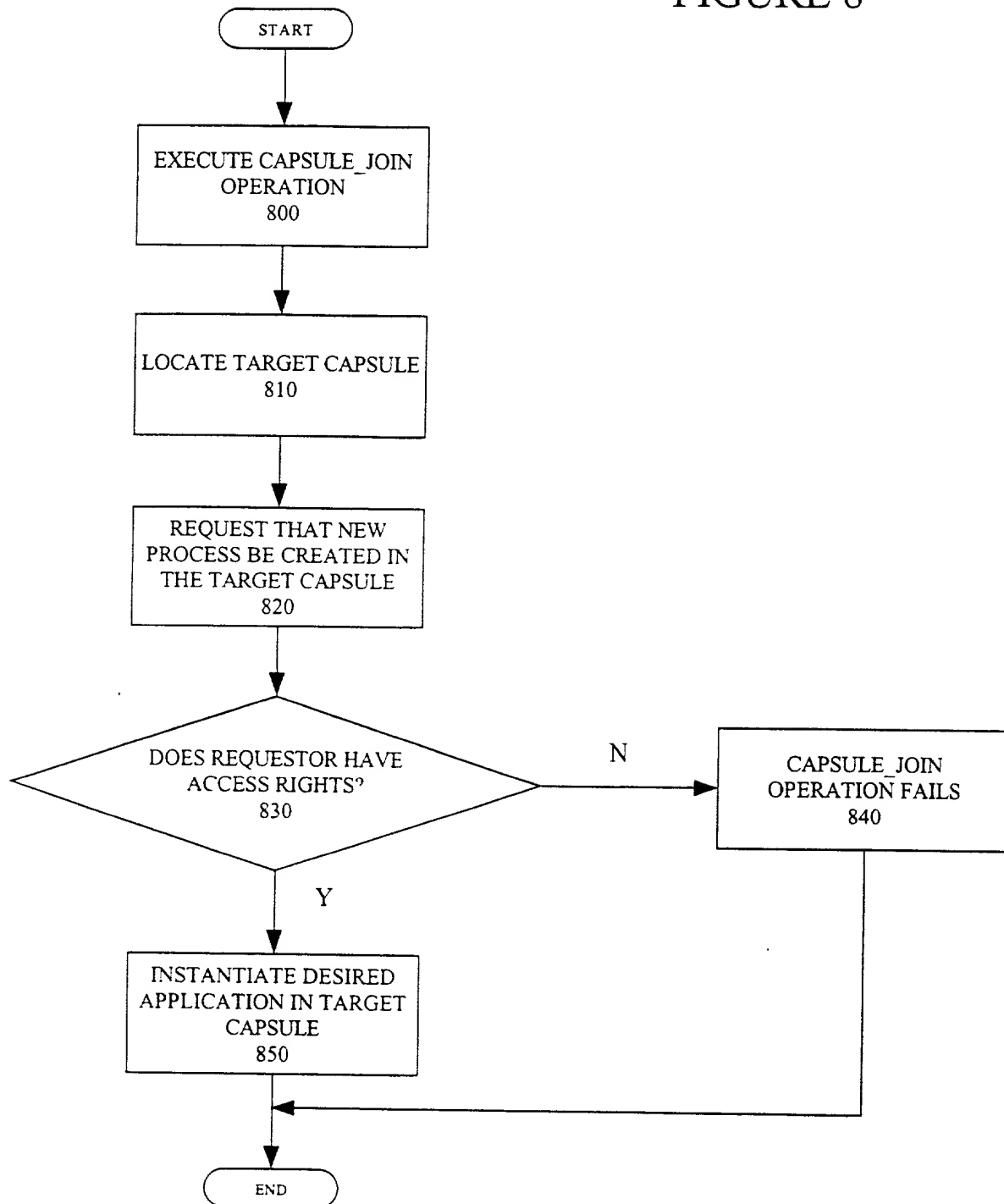


FIGURE 9

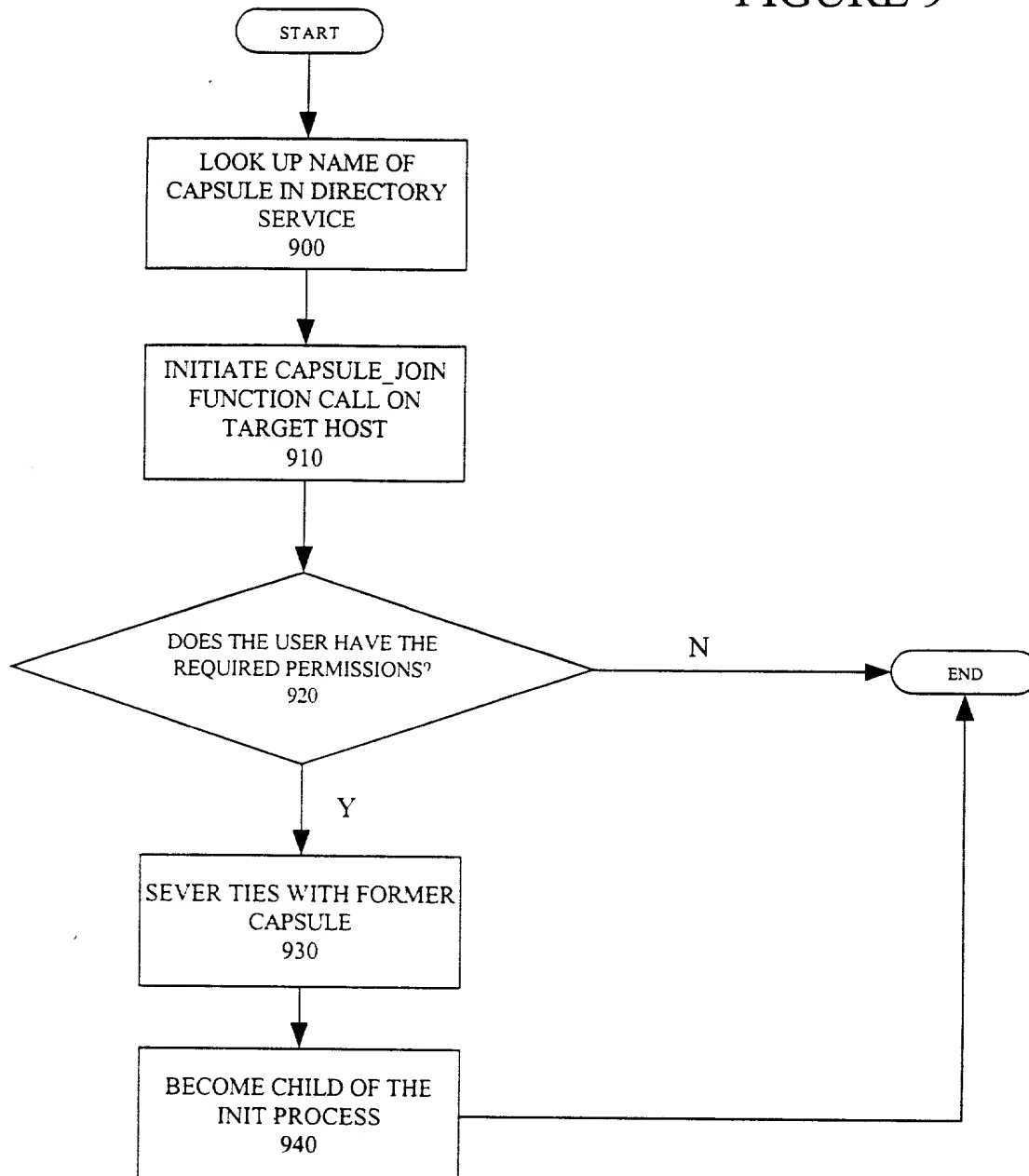
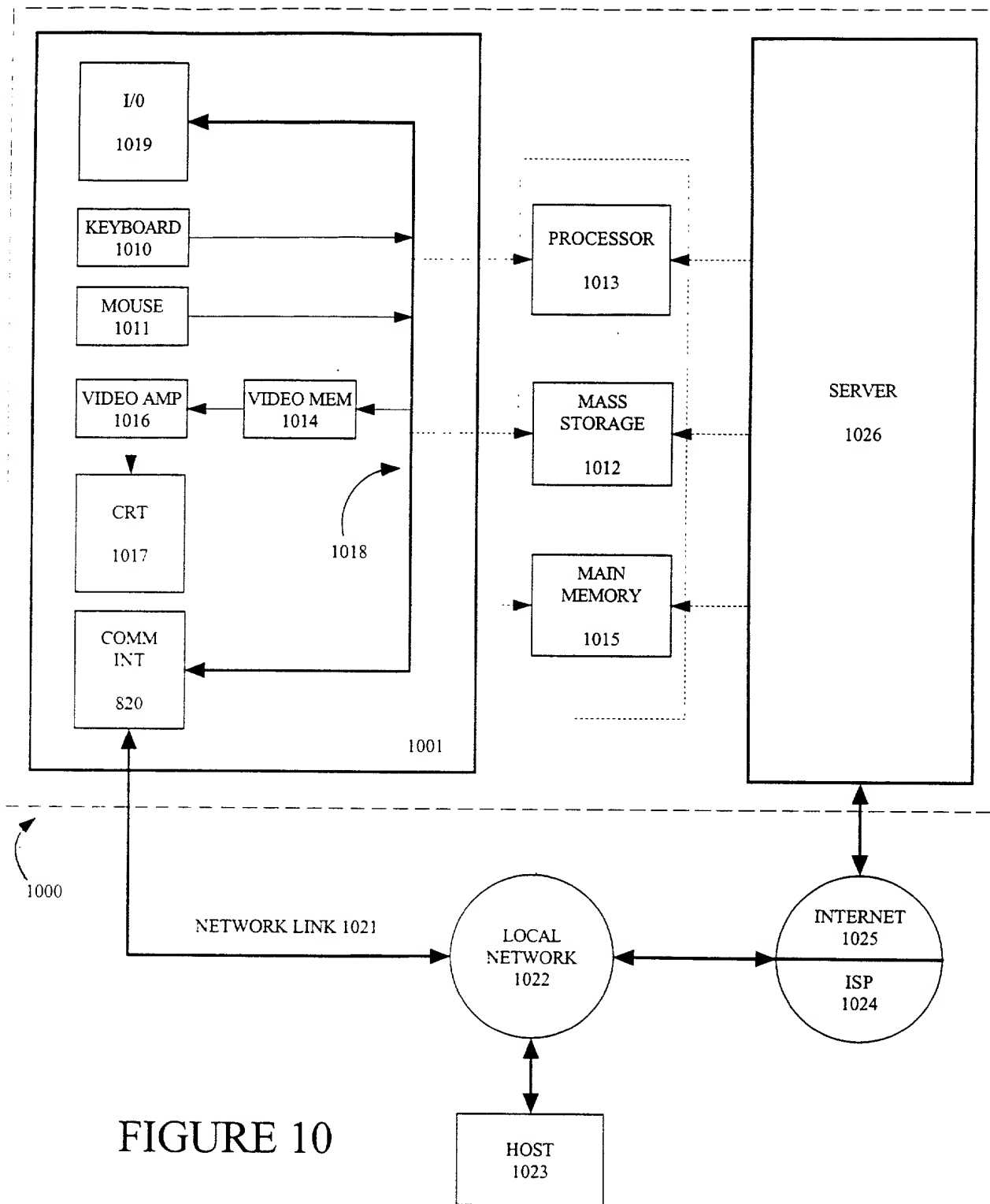


FIG. 10



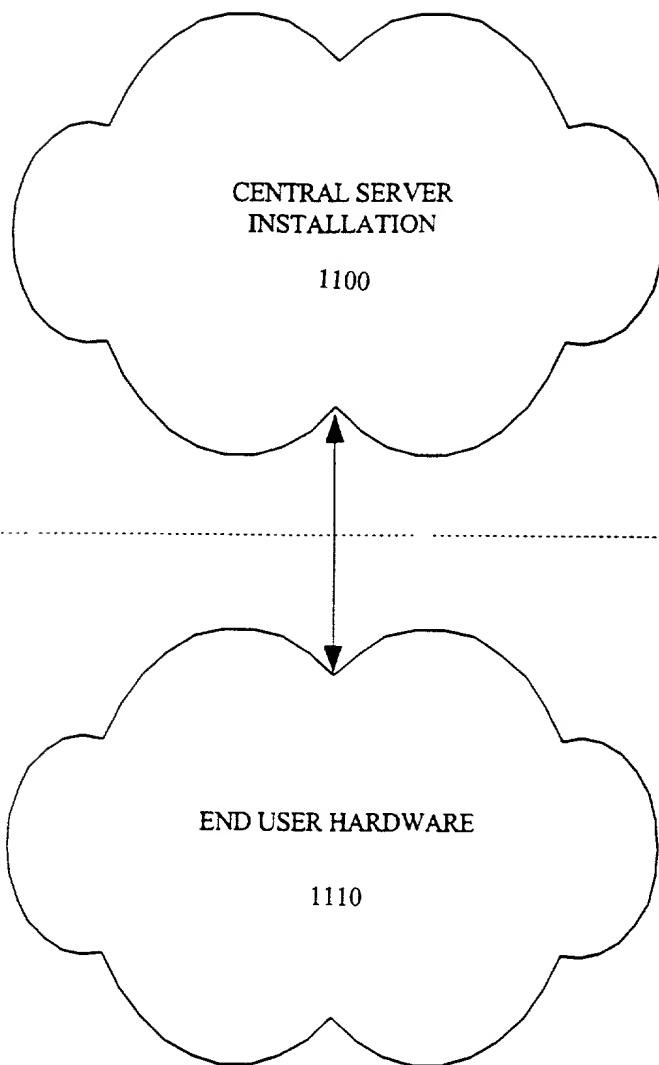


FIGURE 11

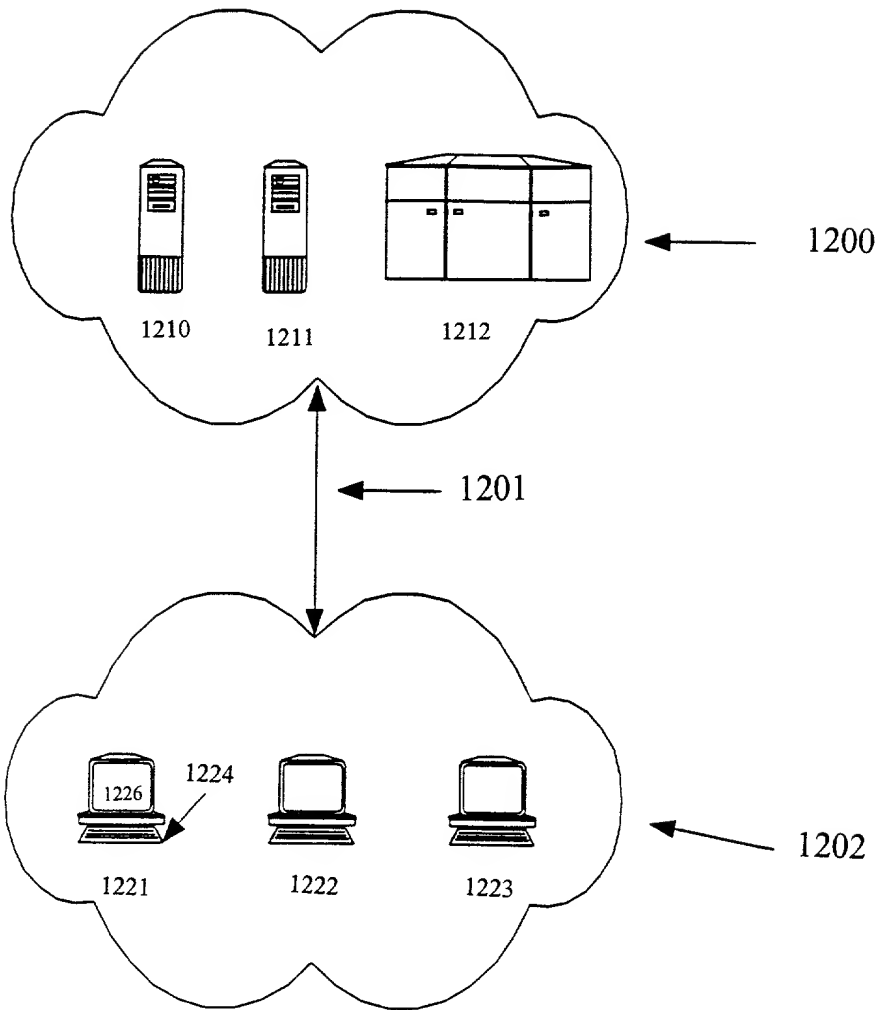


FIGURE 12

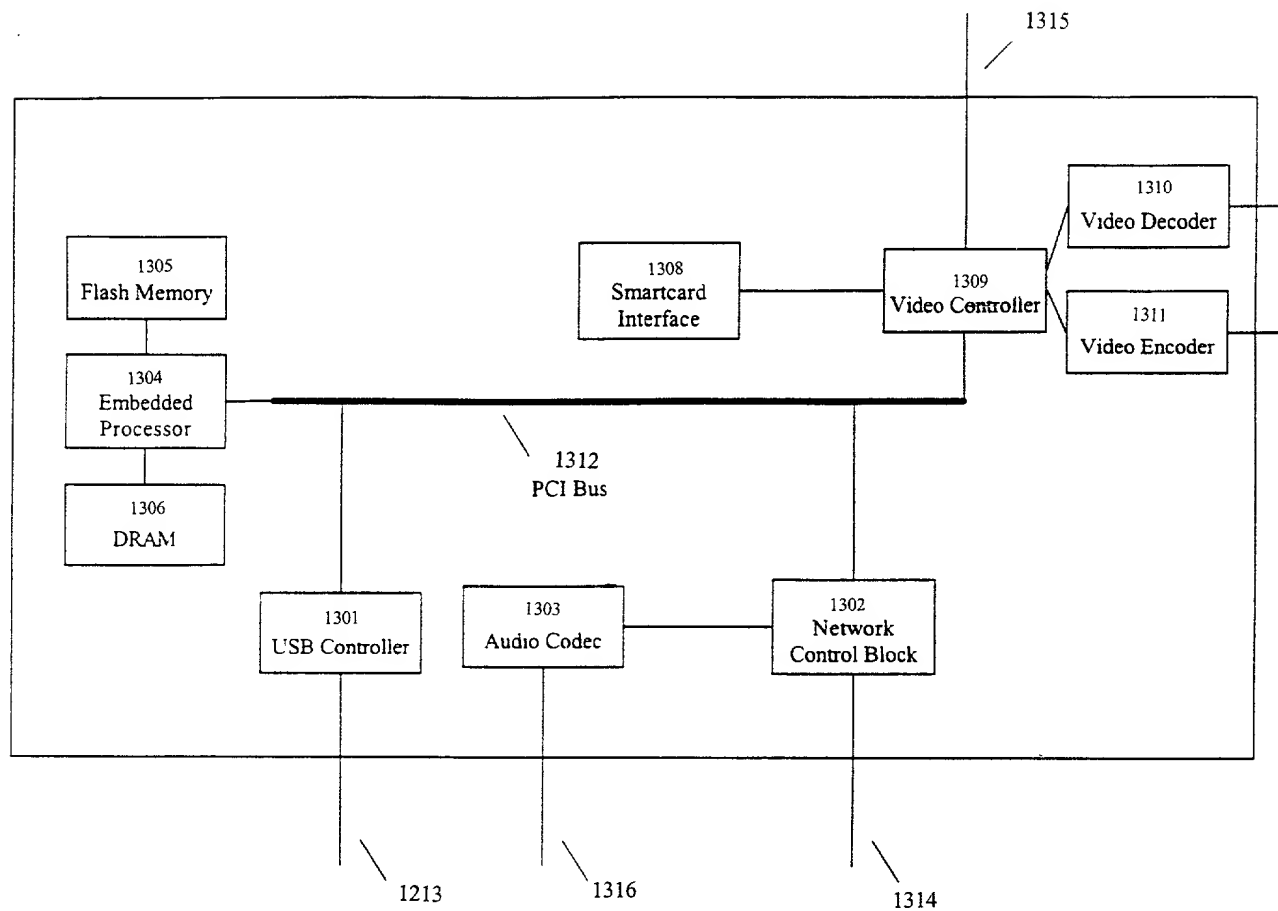


Figure13: HID

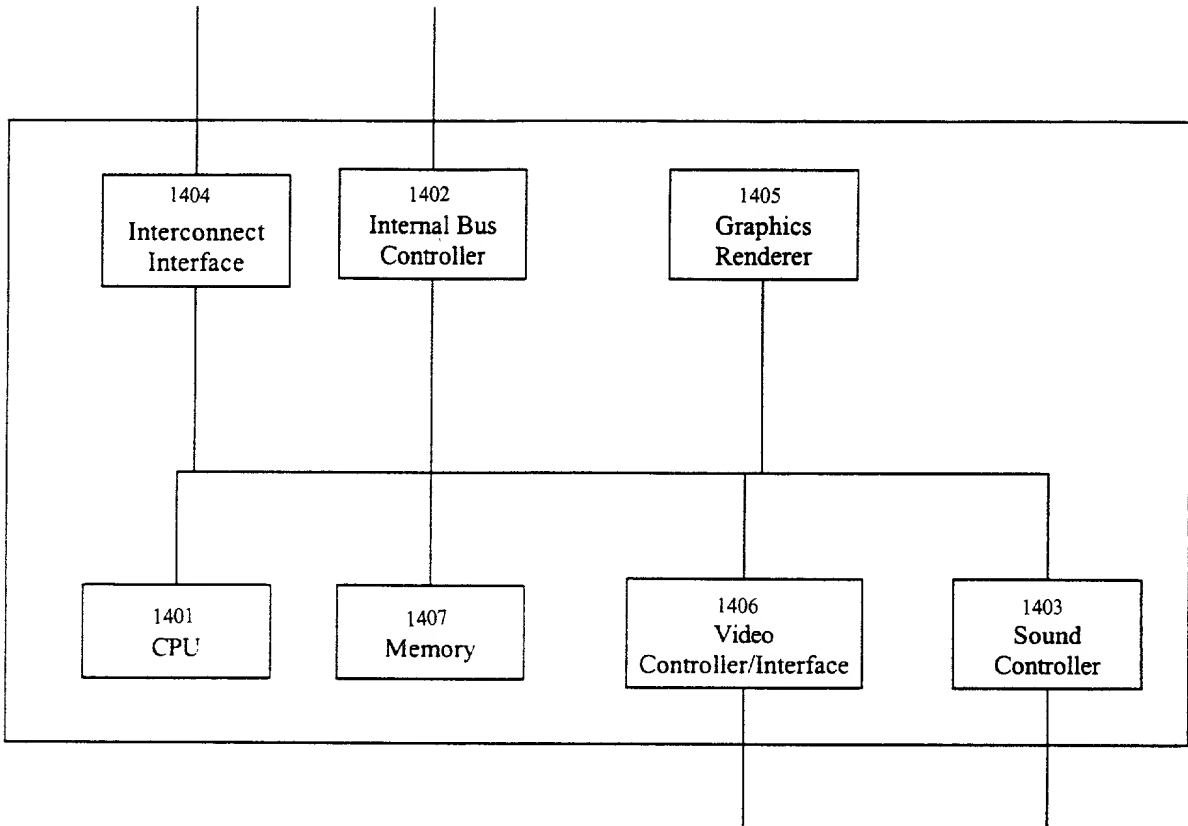


Figure 14: HID Single Chip Implementation

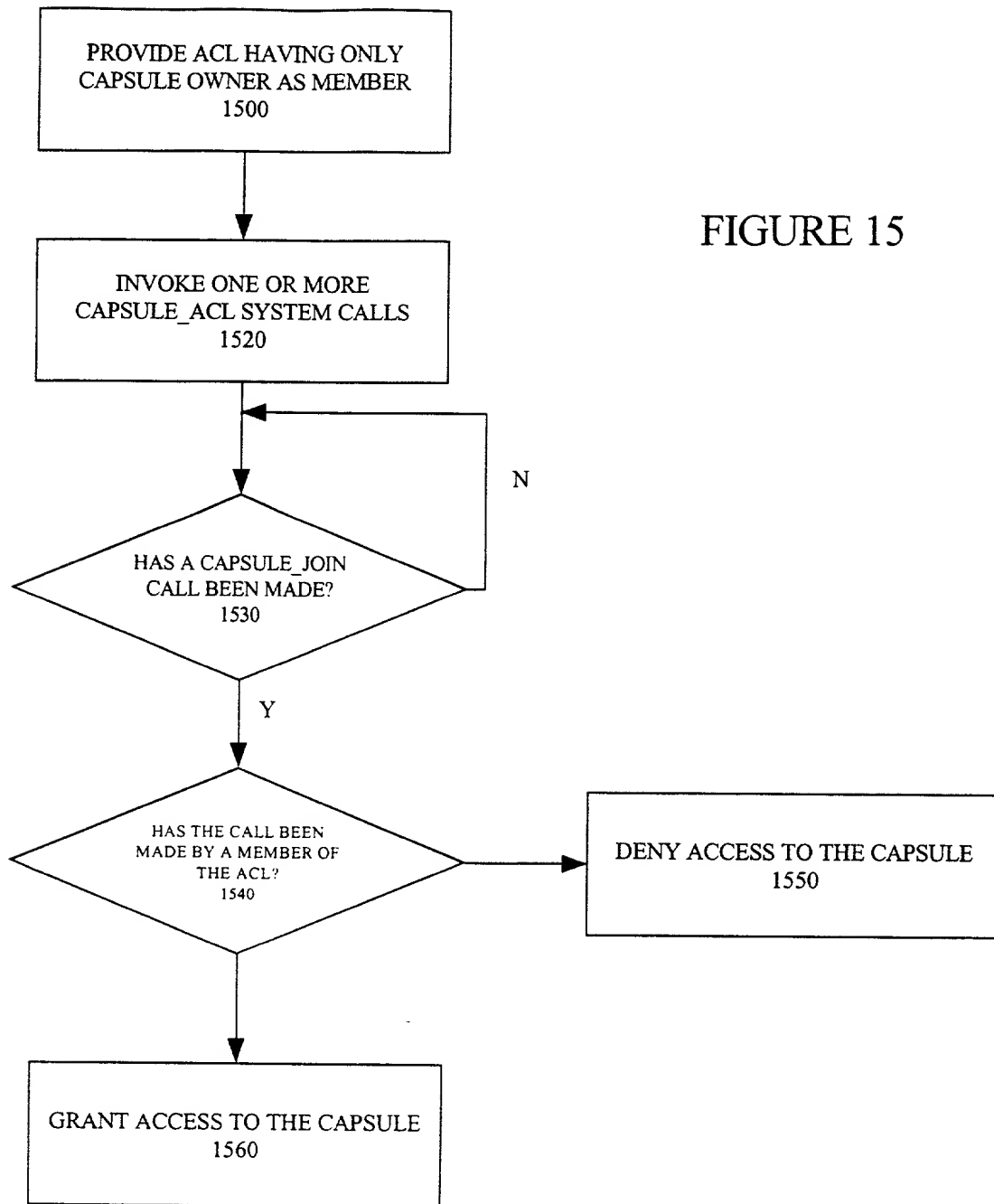


FIGURE 15